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LSI Logic Corporation  
Legal Department - IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035

Date February 24, 2006

Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2825

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191  
No.

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## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please comment

Application Number: 10/697,357  
Filing date: October 29, 2003  
First named inventor: Viswanathan Lakshmanan et al.  
Attorney docket number: 03-1772 / 81610

Transmitted herewith for filing via facsimile:

- Status Request Letter

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2/24/06 Connie del Castillo  
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FEB 24 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Viswanathan Lakshmanan et. al.

Serial No. :

10/697,357

Group Art Unit :

2825

Filed :

October 29, 2003

Examiner :For :Method Of Partitioning An  
Integrated Circuit Design For  
Physical Design VerificationAtty Docket :

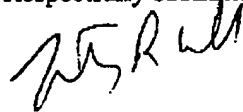
81610 / 03-1772

STATUS REQUESTCommissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail  
the status notification to the address stated below.

Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

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Date: 23 Feb 06